## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Kenichi ADACHI

Application No.: 10/589,380

Filing Date: August 15, 2006

Attornev Docket No.: 545/73

For: Processor, Multiprocessor System, Processor System, Information Processing Apparatus, and

Temperature Control Method

## INFORMATION DISCLOSURE STATEMENT

VIA EFS Mail Stop: Amendment Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant respectfully submits this Information Disclosure Statement, identification of the information, and copies thereof if required pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98 in order to comply with the duty of disclosure. The enclosed Form PTO SB/08A/B identifies the references of which Applicant is aware. The Examiner is respectfully requested to consider the information herein and make it of record in the subject application.

∑ By checking this paragraph, this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b) within three months of the filling date of the application or before the application has received a substantive Office Action. Accordingly, no fee is necessary; however, should the Patent Office determine that a fee is due, the Commissioner is authorized to charge any such fee to Deposit Account No. 11-0223.

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Dated: December 18, 2006 Respectfully submitted,

By: SMatthew B. Dernier/
Matthew B. Dernier
Registration No. 40,989
KAPLAN GILMAN GIBSON & DERNIER LLP
900 Route 9 North
Woodbridge, New Jersey 07095
Telephone (732) 634-7634
Attorners for Applicant

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Application Number 10/589,380 Filing Date August 15, 2006 First Named Inventor Kenichi ADACHI Art Unit Not Yet Assigned Examiner Name Not Yet Assigned Attorney Docket Number 545/73

			U. S. PATENT D	OCUMENTS	
Examiner Initials*	No.1	Document Number  Number-Kind Code <sup>2 (Fixed)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
	GA	International Preliminary Report on Patentability to corresponding PCT/JP2004/019208, dated 11/29/2006, 5 pgs.	1
	СВ	BENINI et al.; "Reducing Power Consumption of Dedicated Processors Through Instruction Set Encoding," VLSI, 1998, Proceedings of 8th Great Lakes Symposium on IEEE, pp. 8-12.	1
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